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Automatic Reactive Power Compensation System

Jay Mahesh Khadse[1], Vivek Manoj Anasane[2], Aditi Sunil Pawar[3], Dr. Atul Barhate[4]

[1,2,3] UG Student

[4] Assistant Professor

Department of Electrical Engineering, Godavari College of Engineering, Jalgaon, India, 425001

Abstract – *In this proposed system, two zero crossing detectors are used for detecting zero crossing of voltage and current. The project is designed to minimize penalty for industrial units using Automatic Reactive Power Compensation unit. The PIC microcontroller used in this project.*

The time lag between the zero-voltage pulse and zero-current pulse is duly generated by suitable operational amplifier circuits in comparator mode is fed to two interrupt pins of a microcontroller. The program takes over to actuate appropriate number of relays from its output to bring shunt capacitors into load circuit to get the power factor till it reaches near unity. The capacitor bank and relays are interfaced to the microcontroller using a relay driver. It displays time lag between the current and voltage on an LCD. Furthermore, the project can be enhanced by using thyristor control switches instead of relay control to avoid contact pitting often encountered by switching of capacitors due to high in rush current.

INTRODUCTION

In modern electrical systems, the increasing use of inductive loads such as motors, transformers, and industrial equipment results in a low power factor, which leads to higher current flow, increased power losses, and poor system efficiency. Maintaining a high power factor is essential for effective utilization of electrical energy and reduction of operational costs.

Traditional methods of power factor correction, such as fixed capacitor banks and manual switching, are not suitable for varying load conditions. To overcome these limitations, Automatic Reactive Power Compensation Systems have been developed, which continuously monitor the system and adjust compensation accordingly.

In this work, a microcontroller-based system using PIC16F877A is implemented to measure voltage and current through Potential Transformer (PT) and Current Transformer (CT). The signals are processed using an Analog-to-Digital Converter (ADC) to determine the phase difference and calculate the power factor. Based on this, capacitor banks are automatically switched using relays to provide reactive power compensation.

The proposed system offers a low-cost, efficient, and reliable solution for improving power factor in both industrial and domestic applications.

LITERATURE REVIEW

Power factor correction is an important aspect of modern power systems to improve efficiency and reduce losses caused by inductive loads. Traditional methods used fixed capacitor banks and manual switching, which were not effective under varying load conditions and often resulted in over- or under-compensation, as discussed in Electrical Technology.

With the advancement of embedded systems, automatic power factor correction (APFC) techniques using microcontrollers have been developed. Studies based on Power Electronics: Circuits, Devices and Applications show that microcontroller-based systems can continuously monitor system parameters and provide real-time compensation. In such systems, microcontrollers like ATmega8A are widely used due to their built-in ADC, timers, and low-cost implementation.

For accurate measurement, Current Transformer (CT) and Potential Transformer (PT) are used to step down current and voltage signals to safe levels. These analog signals are processed through ADC and analyzed using zero-crossing detection techniques to determine the phase difference between voltage and current, enabling calculation of power factor.

However, existing systems face limitations such as switching delays, sensor inaccuracies, and reduced performance in non-linear loads. Therefore, there is a need for a reliable, low-cost, and efficient automatic reactive power compensation system, which is addressed in the proposed work. However, existing systems face challenges such as switching delays, sensor inaccuracies, and poor performance under non-linear loads. Therefore, there is a need for a low-cost, accurate, and efficient automatic reactive power compensation system.

METHOLOGY

The proposed Automatic Reactive Power Compensation System uses the ATmega8A as the main control unit to monitor and improve the power factor. The line voltage and load current are sensed using Potential Transformer (PT) and Current Transformer (CT), which step down the high voltage and current to safe analog levels. These signals are conditioned and fed into the built-in Analog-to-Digital Converter (ADC) of the ATmega8A for digital conversion. Zero Crossing Detection (ZCD) circuits are used to detect the zero-crossing points of voltage and current waveforms, and the time difference between these signals is measured using internal timers of the microcontroller. This time delay is used to calculate the phase angle and power factor using cosine relationships. Based on the calculated power factor, the microcontroller executes a control algorithm to switch capacitor banks through a relay driver circuit. The capacitor banks provide leading reactive power to compensate for lagging reactive power of inductive loads. The system continuously operates in a closed-loop manner, adjusting compensation dynamically with changing load conditions. A crystal oscillator is used to provide a stable clock signal, ensuring accurate timing and reliable system performance.

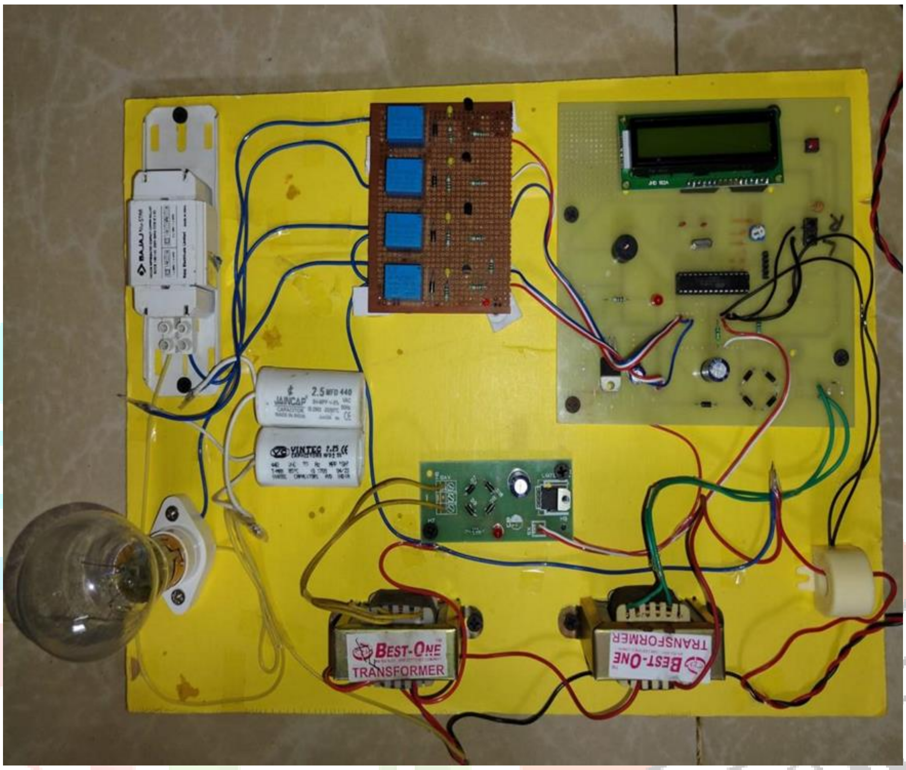

Fig. 1- Project Interface.

Advantages of ATmega8A in your project:

- Built-in 10-bit ADC → no external ADC needed
- Easy programming (Embedded C / Arduino style)
- Low cost and easily available
- Good number of I/O pins for relays
- Internal timers → accurate time delay measurement
- Works perfectly with crystal oscillator

DESIGN

The proposed Automatic Reactive Power Compensation System is designed using a modular approach consisting of sensing, signal conditioning, control, and compensation units. The core of the system is the ATmega8A, which performs measurement, computation, and control operations.

The sensing unit includes a Potential Transformer (PT) and Current Transformer (CT) to step down high voltage and current from the supply line to safe, low-level signals. These signals are passed through signal conditioning circuits (scaling, filtering, and biasing) to make them suitable for the microcontroller input.

The conditioned analog signals are fed into the built-in ADC of the ATmega8A, where they are converted into digital values. Additionally, Zero Crossing Detection (ZCD) circuits are used to generate digital pulses corresponding to the zero crossings of voltage and current waveforms. The microcontroller uses its internal timers to measure the time difference between these pulses, which is then used to calculate the phase angle and power factor.

The control unit (ATmega8A) executes a programmed algorithm that compares the measured power factor with a predefined reference value. Based on this comparison, it determines the required reactive power compensation.

The compensation unit consists of a capacitor bank connected through a relay driver circuit. The microcontroller controls the relays to switch capacitor banks ON or OFF in steps, thereby supplying leading reactive power to compensate for the lagging reactive power of inductive loads.

A power supply unit is also designed using a step-down transformer, bridge rectifier, filter capacitor, and voltage regulator to provide a stable DC supply to the control circuit. A crystal oscillator is used to provide a precise clock signal to the microcontroller

for accurate timing and operation.
Overall, the system operates in a closed-loop manner, continuously monitoring the load conditions and dynamically adjusting the capacitor bank to maintain the power factor close to unity, ensuring efficient and reliable performance.

COMPONENT

1. Microcontroller
ATmega8A
- Acts as the brain of the system
- Reads voltage and current signals using ADC
- Calculates power factor
- Controls relay switching for capacitor banks

2. Potential Transformer (PT)
Potential Transformer
- Steps down high AC voltage (230V) to low AC voltage
- Provides safe voltage signal to the circuit
- Used for voltage sensing

3. Current Transformer (CT)
Current Transformer
- Steps down high current to low current
- Converts load current into measurable signal
- Used for current sensing

4. Analog-to-Digital Converter (ADC)
- Built inside ATmega8A
- Converts analog signals (from PT & CT) into digital form
- Enables microcontroller to process signals

5. Zero Crossing Detector (ZCD)
- Detects zero-crossing points of voltage and current signals
- Helps measure time difference between signals
- Used to calculate phase angle and power factor

6. Relay Driver Circuit
- Interface between microcontroller and relays
- Amplifies control signal
- Uses transistors/IC (like ULN2003)

7. Relay
- Electrically operated switch
- Controls ON/OFF of capacitor banks
- Provides isolation between control and power circuit

8. Capacitor Bank
- Provides leading reactive power
- Compensates lagging reactive power of inductive loads
- Improves power factor

9. Step-Down Transformer
- Converts 230V AC to low voltage (e.g., 12V AC)
- Used in power supply section

10. Bridge Rectifier
- Converts AC to DC
- Uses four diodes
- Provides DC input to regulator

11. Filter Capacitor
- Smoothens pulsating DC
- Reduces ripple

12. Voltage Regulator (e.g., 7805)
- Provides constant 5V DC
- Supplies stable voltage to microcontroller

13. Crystal Oscillator
- Provides clock signal to microcontroller
- Ensures accurate timing and operation

14. Resistors and Capacitors
- Used for signal conditioning
- Voltage division, filtering, and biasing

15. Load (Inductive Load)
- Motor, fan, or any inductive device
- Causes low power factor (lagging)

WORKING PRINCIPLE

Power factor correction systems have been widely studied and implemented in both industrial and commercial settings, particularly in environments where large inductive loads like motors, transformers, and inductive heating elements are prevalent. These loads consume not only real power but also reactive power, which does not contribute to useful work and causes inefficiencies.

Several studies and applications of capacitor-based power factor correction systems have demonstrated significant improvements in power quality and energy savings. These systems typically involve connecting capacitors in parallel with inductive loads, thereby providing reactive power locally and reducing the overall demand from the utility grid. Capacitors compensate for the lagging current caused by inductive loads, thus improving the overall power factor.

In the earlier designs, power factor correction was performed manually, where fixed capacitors were installed to correct the power factor. These systems, though effective, had limitations, especially when loads were not constant. For variable loads, manual capacitor banks led to either overcompensation or under-compensation, resulting in inefficiencies. The transition to automatic systems emerged from this limitation, where capacitor banks could be switched on or off based on real-time power factor measurements.

BLOCK DIAGRAM

1. A simplified block diagram of the APFC system is as follows:
2. Power Supply Unit: Provides regulated power to all components.
3. Current Sensing Circuit: Detects current drawn by the load and sends data to the microcontroller.
4. ATmega328 Microcontroller: Processes current and voltage data, calculates power factor, and controls relays.
5. Relay Board and Capacitor Bank: The relay board activates the capacitor bank to provide reactive power for power factor correction.
6. Toggle Switch: Allows selection between inductive-capacitive and resistive loads.
7. LCD Display: Displays the current power factor and correction status.
8. This setup allows for efficient, real-time power factor correction with minimal user intervention, showcasing a robust embedded system design.

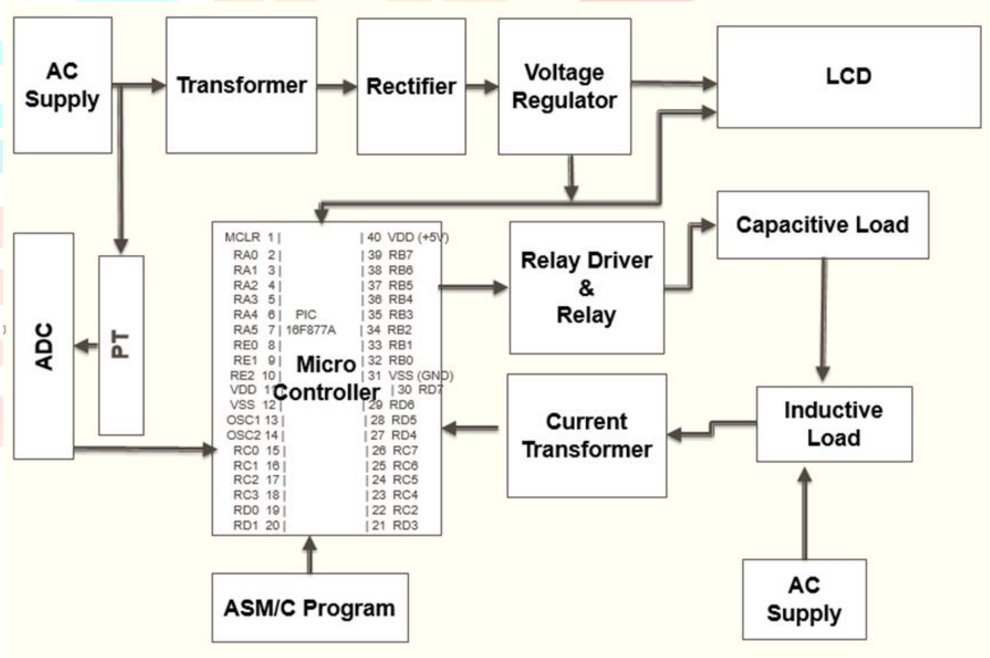

Fig .2-Block Diagram

FUTURE SCOPE

The developed Automatic Reactive Power Compensation System demonstrates effective improvement of power factor using a microcontroller-based approach. However, with the growing complexity of electrical loads and the evolution of smart power systems, there are several opportunities to enhance and extend the proposed design.

A key improvement lies in faster and more reliable switching techniques. The current relay-based capacitor switching can be replaced with solid-state devices such as TRIACs, IGBTs, or solid-state relays to achieve near-instantaneous response, eliminate mechanical wear, and reduce switching transients. This would significantly improve system durability and performance under rapidly changing load conditions.

The system can also be expanded from single-phase to three-phase implementation, which is essential for industrial environments. A three-phase APFC system would require phase-wise sensing, independent compensation control, and balanced capacitor switching, making it more robust for handling complex and unbalanced loads.

Another major enhancement is the inclusion of harmonic detection and mitigation. Modern loads like variable frequency drives (VFDs), SMPS, and power electronics introduce harmonics that degrade power quality. Future designs can incorporate passive filters (LC filters) or active harmonic filters along with power factor correction to ensure both improved PF and reduced harmonic distortion.

The processing capability can be upgraded by replacing the ATmega8A with more advanced microcontrollers or digital signal processors (DSPs). This would allow high-speed sampling, precise phase angle calculation, RMS computation, and implementation of advanced control algorithms for more accurate and stable operation.
Integration with IoT (Internet of Things) platforms offers significant potential for remote monitoring and control. By adding communication modules (such as Wi-Fi or GSM), system parameters like voltage, current, power factor, and capacitor status can be monitored in real time through mobile or web applications. This enables centralized energy management and supports smart grid applications.
Further, the system can be enhanced with data logging and analytics capabilities. Historical data can be stored and analyzed to study load patterns, detect abnormalities, and optimize system performance. This also supports predictive maintenance by identifying component degradation (such as capacitor aging or relay failure) before faults occur.
An advanced improvement is the development of adaptive and intelligent control algorithms. Techniques such as fuzzy logic, neural networks, or machine learning can be used to dynamically decide the optimal level of compensation based on varying load conditions, instead of relying on fixed threshold-based control. This will improve accuracy and reduce the chances of over- or under-compensation.
Future systems can also include automatic capacitor sizing and fine-step control, where instead of switching fixed capacitor banks, the system dynamically selects or combines capacitance values to match the exact reactive power requirement. This leads to smoother and more precise power factor correction.
Integration with renewable energy sources, such as solar photovoltaic systems, is another promising direction. In hybrid systems, reactive power management becomes more critical, and the proposed system can be extended to manage both grid and renewable energy interactions efficiently.
Finally, additional protection and safety features can be incorporated, such as overvoltage protection, overcurrent protection, short-circuit detection, and thermal monitoring. These features will enhance system reliability and make it suitable for real-world industrial deployment.

CONCLUSION

The proposed Automatic Reactive Power Compensation System successfully demonstrates an efficient method for improving power factor using a microcontroller-based approach. By utilizing the ATmega8A along with Potential Transformer (PT) and Current Transformer (CT), the system is able to accurately monitor voltage and current parameters and determine the phase difference between them. Based on this analysis, capacitor banks are automatically switched using relays to provide the required reactive power compensation.

The system operates continuously and adapts to changing load conditions, maintaining the power factor close to unity. This results in reduced power losses, improved voltage regulation, and enhanced overall efficiency of the electrical system. Additionally, the use of simple and cost-effective components makes the design suitable for both industrial and domestic applications.

Overall, the developed system provides a reliable, economical, and practical solution for automatic power factor correction, with potential for further improvements such as faster switching devices and harmonic compensation techniques.

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