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FPGA BASED HOME AUTOMATION

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Abstract: Focusing on FPGA-based home automation with the Basys 3 board, this project integrates sensors for temperature monitoring, door status detection, and remote-controlled operation of an LCD projector. Verilog programming facilitates real-time temperature monitoring, activating fans as necessary. Reed sensors detect door openings, automatically triggering light activation for enhanced convenience and security. Through FPGA control, users can remotely operate the LCD projector, enhancing home entertainment system flexibility and accessibility.

Index Terms **– Home automation, FPGA board, Verilog, security**

I. INTRODUCTION

The modern era witnesses the convergence of advanced technologies, ushering in an era of smart living. Homes equipped with intelligent systems redefine comfort, convenience, and energy efficiency. Among these, Field-Programmable Gate Arrays (FPGAs) offer versatility and adaptability, providing a robust platform for sophisticated automation solutions.

In this paper, we present an FPGA-based home automation system developed using the Basys 3 board, aimed at addressing the diverse needs of contemporary households through innovative sensor integration and intelligent control mechanisms. A fundamental aspect of our FPGA-based home automation system is real-time temperature monitoring. By integrating temperature sensors with the Basys 3 board, our system offers homeowners insights into ambient temperature levels. This enables optimization of indoor comfort and implementation of efficient climate control strategies, contributing to energy savings by ensuring judicious use of heating and cooling systems.

In addition to temperature monitoring, our system integrates intelligent lighting control mechanisms triggered by door activity. Through reed sensors and FPGA logic, the system detects door openings and adjusts lighting accordingly. This proactive lighting automation enhances convenience and energy efficiency, tailoring illumination to household occupancy patterns while minimizing unnecessary energy consumption.

Our FPGA-based solution extends its capabilities to include advanced functionalities such as remote-controlled LCD projection. Utilizing the processing power of the Basys 3 board and wireless communication protocols, users can remotely activate and control an LCD projector, transforming any space into a versatile multimedia environment. This feature adds entertainment value and demonstrates the flexibility and scalability of FPGA-based home automation solutions.

Central to implementing our FPGA-based home automation system is the use of Verilog programming language. Leveraging the flexibility and scalability of Verilog, we developed a robust codebase orchestrating system components with precision and efficiency. This programming paradigm empowers us to harness FPGA technology's full potential, customizing system functionality to suit specific user requirements for seamless integration and optimal performance.

II. SYSTEM LEVEL MODELLING

The system model utilizes the Basys 3 FPGA board as the central processing unit to manage sensor inputs and control outputs. Sensors, including temperature sensors for climate monitoring and reed sensors for detecting door status, interface directly with the FPGA.

For temperature monitoring, the FPGA continuously reads sensor data and processes it using predefined thresholds to trigger fan activation when temperatures exceed specified limits. Similarly, reed sensors provide input to the FPGA, allowing the system to respond to door openings by controlling connected lighting fixtures.

Additionally, the FPGA manages the interface with an LCD projector for remote-controlled multimedia display. By interpreting wireless commands received from a remote or mobile application, the FPGA activates the LCD projector and coordinates the selection and playback of multimedia content.

The entire system operates through the FPGA's programmed logic, which handles sensor data interpretation, decision-making based on predefined rules, and actuation of connected devices. This approach ensures efficient and responsive home automation tailored to user preferences and environmental conditions.

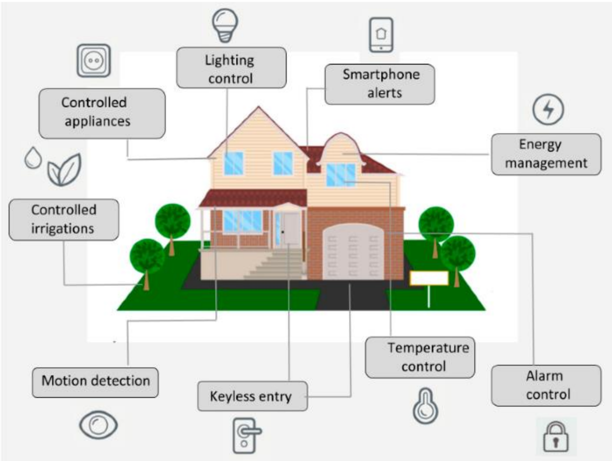

Fig 1: Model

All sensors used in the system, including the IR sensor and the reed sensor, are directly integrated into the FPGA board. The FPGA board serves as the physical support for the development platform, offering seamless integration with standard FPGA synthesis and implementation tools. Specifically, the system utilizes the latest Artix-7 Field Programmable Gate Array (FPGA) from Xilinx, mounted on the Basys 3 board. This FPGA board features 1,800 Kbits of fast block RAM, an on-chip analog-to-digital converter, 16 user switches, 16 user LEDs, and a 4-digit 7-segment display. The signal from the IR sensor is processed through the Pmod JB input port.

III. LITERATURE SURVEY

In this literature survey we embark on an exploration of the burgeoning field of FPGA-based home automation systems, aiming to elucidate the current state-of-the-art and identify key trends and challenges. First paper we reviewed “FPGA—based assistive framework for smart home automation” Md Sharif Ahmed, Ratri Mukherjee. Prosenjit Ghosh, SK Nayemuzzaman Department of Electrical Engineering 2022 IEEE l5th Dallas Circuit and System Conference (DCAS)| 978-1-6654-9885-2/22/$31.00 Ö 2022 IEEE| DOI: 10.1109/DCAS53974 .2022.9845625, this study presents a reconfigurable framework for automated home security and monitoring, leveraging FPGA technology and integrating sensors for fire safety and anti-theft measures.

“FPGA-based embedded architecture for IoT home automation application” Chee-Pun. Ooi, Wooi-Haw. Tan, Soon-Nyean. Cheong, Yee-Lien. Lee, V. M. Baskaran, Yeong-Liang. Low Faculty of Engineering, Multimedia University, Malaysia Indonesian Journal of Electrical Engineering and Computer Science ρ 646

Vol. 14, No. 2, May 2019, pp. 646~652 ISSN: 2502-4752, DOI: 10.11591/ijeecs. v14.i2. pp646-652, This project presents an IoT FPGA-based home hub for automating home operations, leveraging FPGA technology and an IoT platform. Through Verilog HDL programming and WIFI extension, it enables sensor integration and user-configurable control rules for diverse home environments.

"Home Automation through FPGA Controller" Madhuri R Mukkawar Student, Dept.of E&TC NBN Sinhghad School Of Engineering Pune, India International Journal of Engineering Research & Technology (IJERT) ISSN: 2278-0181 Vol. 3 Issue 3, March – 2014, this paper delves into the integration of network-enabled digital technologies for advancing home automation, presenting a design implemented in VHDL on FPGA. Leveraging GSM communication and SMS-based interaction, the system aims to enhance living standards through wireless technology.

"IOT BASED HOME AUTOMATION USING FPGA" 2022 IJCRT | Volume 10, Issue 4 April 2022 | ISSN: 2320-2882, IoT has transformed home automation, allowing smartphone control of appliances and long-distance data exchange via Wi-Fi, benefiting users with enhanced accessibility and precision.

"IoT based home automation using FPGA" International Journal of Advanced Research, Ideas and Innovations in Technology ISSN: 2454-132X Impact Factor: 6.078 (Volume 7, Issue 5 - V7I5-1243), IoT technology is reshaping lifestyles, enabling seamless control of home appliances via smartphone apps and Wi-Fi connectivity, facilitated by FPGA for secure and flexible device integration.

IV. Methodology

The methodology for implementing the FPGA-based home automation system involves several key steps. First, the hardware components, including the Basys 3 FPGA board, temperature sensors, reed sensors, and LCD projector, are assembled and connected according to the system design.

Next, the FPGA programming environment, such as Vivado, is utilized to develop the hardware description language (HDL) code, specifically Verilog, for interfacing with the sensors and controlling the output devices. This code defines the behavior of the FPGA in response to sensor inputs and external commands.

Once the Verilog code is developed, it is synthesized, implemented, and verified using simulation tools to ensure correct functionality and performance. This step involves testing the code under various scenarios and conditions to validate its effectiveness in managing sensor data and controlling connected devices.

After verification, the FPGA design is synthesized onto the Basys 3 board, and the hardware components are integrated into the home environment. This includes installing temperature sensors in strategic locations, such as living spaces and bedrooms, and mounting reed sensors on doors to detect openings.

Finally, the system is tested and evaluated in real-world conditions to assess its reliability, responsiveness, and overall performance. Any necessary adjustments or optimizations to the FPGA code or hardware configuration are made based on the test results to ensure optimal functionality of the home automation system.

4.1 Block diagram

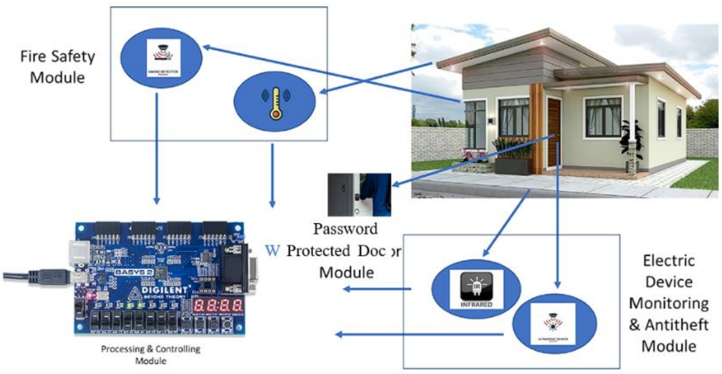

Fig 2. Block diagram of the proposed framework

In this block diagram, home automation is proposed with 2 main features such as Electronic Device monitoring & Antitheft Module, and a Password Protected Door Module. In our block diagram figure 2, a module of the virtual house has been considered. The outputs are initiated to zero. The inputs of our system include room door IR sensor, room windows IR sensor, room door ultrasonic sensor, Door lock module which are essential modules for smart home automation and assisted living. All input signals contain data such as a password to turn ON and OFF security, door, and window are in input to the IR sensor, clocks for clock input, and so on. FSM reset is going to be implied to restart the state machines and transition between each module. The output signals include door condition for opening and closing of a door, window status for opening and closing window, output on entering the wrong password. The FPGA is programmed using the hardware description language, Verilog. FPGA has multiple timing domains and there are plenty of asynchronous errors. These kinds of circumstances occur when different kinds of specific data patterns exist. It is difficult to include multiple sensors in a single FPGA platform due to the lower number of input-output ports. Also, Basys 3 FPGA board does not come with an integrated GSM, Wi-Fi module. Having so many limitations, our main challenge is to incorporate multiple sensors like IR, PIR, Ultrasonic, Temperature, and Gas sensors in a single platform which is rarely seen in the FPGA platform. This research will not only be used in home security but can also be installed in offices, schools, medical hospitals, and others.

IV. RESULTS AND DISCUSSION

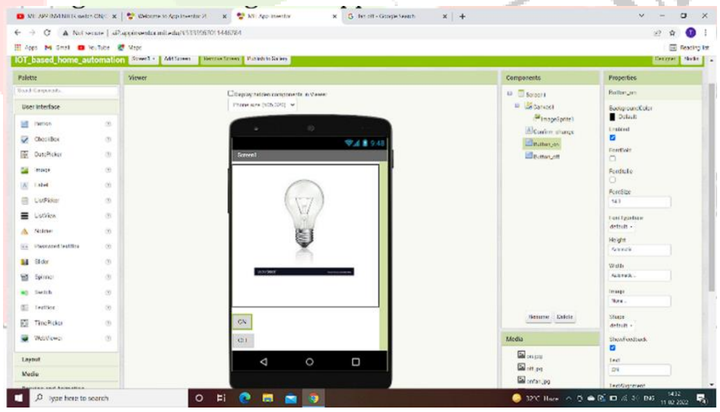

Fig 3. MIT app

The output of the FPGA-based home automation project encompasses both tangible and intangible aspects, culminating in a comprehensive enhancement of the residential living experience. Tangibly, the project delivers a fully operational home automation system adept at monitoring and regulating various facets of the domestic environment. This includes functionalities such as temperature sensing, door status detection, fan control, and remote operation of the LCD projector. Through precise sensing mechanisms and efficient control algorithms, the system demonstrates its capacity to accurately perceive environmental conditions and enact appropriate responses to ensure optimal comfort and convenience for occupants.

Beyond the realm of physical functionalities, the project yields intangible benefits that significantly augment the quality of life within the home. Through seamless integration with the MIT application, users gain unprecedented levels of convenience and accessibility in managing household devices remotely. Whether adjusting thermostat settings on-the-go or activating lighting systems upon entry, the automation system streamlines daily routines, fostering a more comfortable and efficient domestic lifestyle. Moreover, by automating repetitive tasks and optimizing resource utilization, the system contributes to improved energy efficiency and sustainability practices, aligning with contemporary trends towards eco-conscious living.

In essence, the output of the FPGA-based home automation project extends far beyond mere technological prowess, encompassing a holistic transformation of the residential living environment. By seamlessly intertwining cutting-edge sensor technologies, intelligent control mechanisms, and user-friendly interfaces, the system not only empowers users with unprecedented levels of control and convenience but also fosters a deeper sense of harmony and well-being within the home. As a testament to the transformative potential of smart automation technology, the project sets the stage for a new era of domestic living characterized by enhanced comfort, efficiency, and sustainability.

V. Conclusion

In conclusion, the FPGA-based home automation project represents a significant leap forward in residential living, offering a comprehensive suite of features designed to enhance comfort, convenience, and energy efficiency. Through seamless integration of sensor technologies, intelligent control algorithms, and user-friendly interfaces, the system provides users with unprecedented levels of control and accessibility, ultimately redefining the modern domestic experience. As smart automation continues to evolve, this project serves as a testament to the transformative potential of technology in shaping the homes of tomorrow.

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