



# DESIGN AND OPTIMIZATION OF A FLYBACK CONVERTER FOR SOLAR PUMP CONTROLLER

<sup>1</sup>Dr. Anwarul M. Haque, <sup>2</sup>Het Patel, <sup>3</sup>Shail Rami, <sup>4</sup>Krutik Panchal, <sup>5</sup>Darshan Patel,

<sup>1</sup>Assistant Professor, <sup>2</sup>Student, <sup>3</sup>Student, <sup>4</sup>Student, <sup>5</sup>Student

<sup>1</sup>Power Electronics Department

<sup>1</sup>Vishwakarma Government Engineering College, Chandkheda, Ahmedabad, Gujarat, India

**Abstract:** The fly-back converter is a popular power electronics converter for regulated power supply due to its simplicity and efficiency. This research paper concentrates on designing a flyback converter for a solar pump controller, considering input voltages from 400V to 1200V DC and output voltages of 15V, 5V, and 3.3V. Implementing the Cascode topology in the converter aims to increase controller performance, minimize losses, and improve voltage control. The Cascode technique improves overall efficiency by optimizing switching characteristics and reducing voltage stress on switching devices. The research analyses the behavior of a flyback converter in Discontinuous Conduction Mode (DCM) for a solar pump controller where the inductor current totally discharges after each switching cycle. The simulation results represent the converter in DCM using the Cascode technique is efficient, generating required output voltages and accurate voltage control over a broad range of input voltage changes. The results are beneficial for engineers and academics working on high-voltage isolated power supply, particularly for solar pump control applications. The paper has a sense on design considerations, simulations, and analysis of flyback converters, with a focus on improving the performance of solar pump controller.

**Key Words – Isolated DC-DC converter, SMPS, Transformer, Power Electronics Switches**

## I. INTRODUCTION

Solar pump controllers are essential components in solar-powered water pumping systems, which are increasingly adopted for their environmental sustainability and cost-effectiveness. These systems utilize solar panels to convert sunlight into electrical energy, which is then used to power the pump for water extraction [12], [13]. The flyback converter, with its galvanic isolation through a transformer, is an ideal choice for solar pump controllers as it ensures safety and protects sensitive electronic components from electrical disturbances [14], [15].

One of the key advantages of using the flyback converter in the discontinuous conduction mode (DCM) is its ability to minimize stress on the switches and diodes. In DCM operation, the current through these components becomes zero during the switching turn-off period, reducing their power dissipation and enhancing overall efficiency. This stress reduction not only improves energy efficiency but also enhances the reliability and durability of the converter components, thereby increasing the lifespan of the solar pump controller [13].

To construct a flyback converter for DCM operation in a solar pump controller, it is crucial to consider load changes and higher energy efficiency. Proper selection and sizing of the switch, output capacitor, diode, and transformer are vital for achieving optimal performance and reliable operation. Additionally, an in-depth understanding of the flyback converter's operation modes, efficiency concerns, component selection, and control strategies is necessary to ensure a successful design.

we provide a comprehensive reference for the design of a high-performance and dependable flyback converter for solar pump controllers. It will discuss the important design criteria, considerations, and factors that need to be taken into account during the development process. By following these guidelines, engineers and researchers can effectively design flyback converters that meet the specific requirements of solar pump controllers, enabling efficient and reliable water pumping systems powered by solar energy.

## II. WORKING OF FLYBACK CONVERTER

A fly-back converter Fig.1 uses a switching transistor, generally a MOSFET for power flow regulation. The input DC voltage is first filtered before it's transmitted to the flyback transformer's primary winding. MOSFET quickly turns on and off, providing a square wave voltage across the primary winding of the transformer.

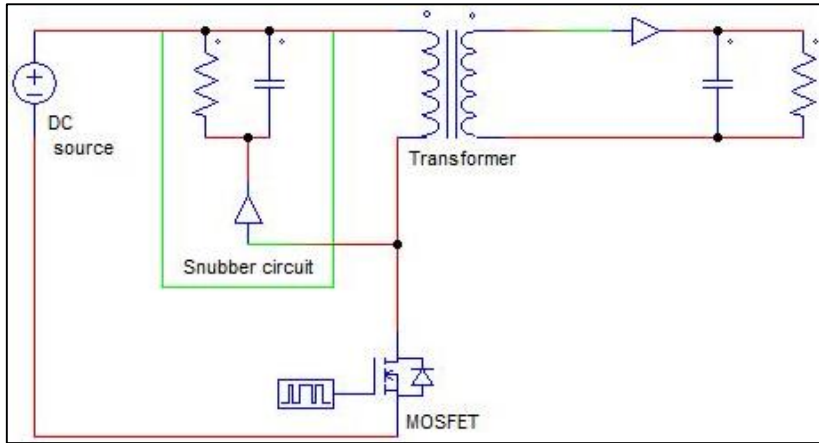


Figure 1: Flyback Converter Circuit Diagram

During the "on" period, energy is stored in the magnetic field of the transformer [1]-[3]. When the MOSFET is turned off, the magnetic field decreases, causing a voltage to be generated in the secondary winding. Then, this voltage is rectified and filtered to provide the necessary DC voltage output. To manage the output voltage, feedback control changes the duty cycle of the MOSFET. Through the transformer, the flyback converter makes galvanic isolation, allowing for the effective conversion of energy and compact power supply designs.

### 2.1 Application

A flyback converter is an essential component in a wide range of systems in industries. It is an essential element in power supply, providing efficient and regulated direct current (DC) power to electronic equipment. Flyback converters in solar pump controllers provide for component protection, galvanic isolation, regulation of voltage and efficient power conversion; all of these characteristics contribute to the overall performance, dependability, and durability of solar-powered water pumping systems. However, flyback converter is also used in industrial converters, automotive components, healthcare products, industrial automation, telecommunications devices, renewable energy systems, army and aviation systems, charging stations for electric car systems, information centers, robotics and automation equipment, and uninterruptible power supplies (UPS). It enables dependable and optimum operation across a wide number of industries by offering efficient power conversion, isolation, and voltage control [4]-[7].

## III. TYPES OF FLYBACK CONVERTER

The flyback converter is Categorized into 2 different parts as per its mode of conduction [17].

### 3.1 CCM (Continuous Conduction Mode)

The CCM mode ensures that the current flowing across the inductor is not reduced to zero during any switching cycle. Benefits of this constant current flow include a steadier output voltage and less output distortion. However, CCM is less suitable for applications demanding high voltage or high power since it requires the use of a larger inductor to control the constant current.

### 3.2 DCM (Discontinuous Conduction Mode)

The DCM mode presents a non-continuous waveform with the inductor current decreasing to zero during each switching cycle. A smaller inductor can be used with DCM, which is advantageous for low-power or low-voltage applications. However, it can cause more output ripple and the need for further noise-reduction filtering techniques.

## IV. SPECIFICATION OF FLYBACK CONVERTER

The requirement of the flyback converter to be used for solar pump controller application is as under.

- Input voltage- 400 to 1200V DC or 230 to 650V AC
- Output power- 50W
- Switching frequency  $\leq 40\text{kHz}$
- Expected efficiency - 70% to 80%
- Output ripple voltage  $\geq 500\text{mV}$
- UVLO and OVLO protection
- Here, designs an example of a flyback converter that can convert the Input AC or DC supply into desired 15V, 5V, and 3.3V Isolated output voltage.

## V. DESIGN A FLOW CHART OF THE FLYBACK CONVERTER

Now as per design requirements, the steps taken into consideration are as below Fig.2 [9]. Design steps of a fly-back converter which convert the Input AC / DC supply into desired 15V, 5V, and 3.3V Isolated output voltage (table-1) is demonstrated as under.

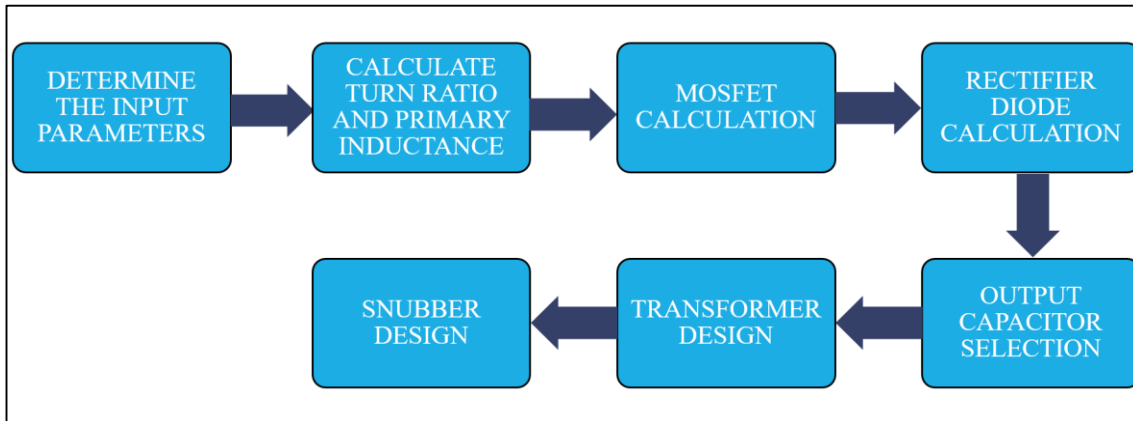


Figure 2: Steps to Design Flyback Converter

### Step 1: Input Design Parameter

Input voltage( $v_{in}$ )	400V to 1200V DC
Output Voltage( $V_{out}$ )	15V, 5V, 3.3V
Output Power( $P_{out}$ )	50W max
Operation mode	DCM
Maximum Duty cycle	0.5
Switching Frequency	50kHz
Estimated efficiency( $\eta$ )	80%
Input Power (as per our efficiency)	62.5W

### Step 2: Calculation of Primary Inductance and Transformer's Turn Ratio

$$\begin{aligned}
 LP &= \frac{\eta \times D_{max}^2 \times V_{in\_min}^2}{(2 \times f_{sw} \times \text{ripple ratio } (K) \times P_o)} \\
 &= 6400 \times 10^{-6} \\
 &= 6400\mu\text{H}
 \end{aligned}$$

Now, for the transformer's turns ratio,

$$N_{s1} = \frac{V_{INMin} \times D_{max}}{(1 - D_{max}) \times (V_o + VD)}$$

Multilevel winding transformers have considered for three different output voltage which has 1 primary, 3 secondary and 1 auxiliary winding.

Here, we have also considered the rectifier diode voltage drop is 0.7V. So, for 3 output levels for transformer, the winding ratios are,

- ✓ For 1<sup>st</sup> secondary winding (15V)- 20.63
- ✓ For 2<sup>nd</sup> secondary winding (5V)- 56.84
- ✓ For 3<sup>rd</sup> secondary winding (3V) – 79.024

**Step 3: MOSFET Calculation**

**Cascode Topology For MOSFET**

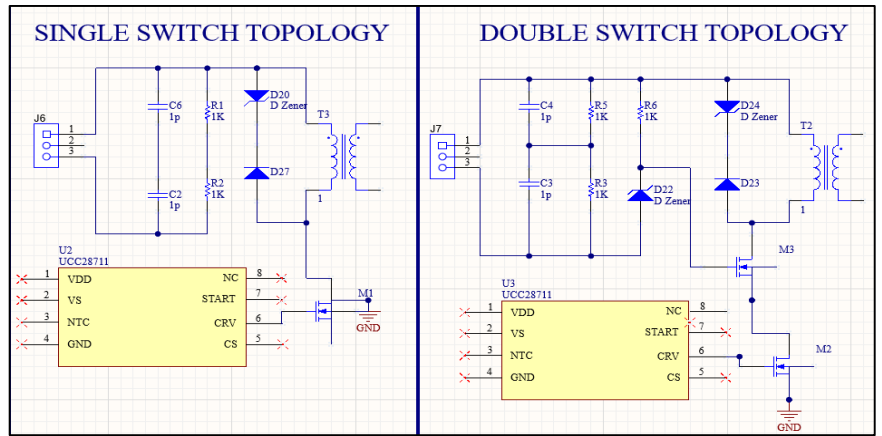
The Cascode method is a technique commonly used in electronic circuits to enhance the performance of MOSFET. It aims to overcome limitations such as restricted bandwidth, limited gain, and output impedance by employing a stacked configuration of two or more MOSFETs.

In the Cascode configuration Fig.3, MOSFETs are connected in series, typically using a common gate or common source setup. The lower MOSFET's output is connected to the upper MOSFET's input, combining the advantages of both transistors to address their limitations [16].

To implement the Cascode method with MOSFETs, follow these steps:

1. Start with a basic MOSFET circuit using a common source or common gate configuration. Connect a load resistor to the drain terminal and a signal source to the gate terminal.

2. Introduce a second MOSFET in a stacked configuration. Connect the drain of the lower MOSFET to the drain of the upper MOSFET. Ground or a bias voltage is connected to the lower MOSFET's source, while the upper MOSFET's source is connected to the load resistor.



**Figure 3: Cascode Topology for Single Switch and Double Switch**

3. Connect the input signal source to the lower MOSFET's gate and apply a bias voltage to the upper MOSFET's gate.

4. Take the Cascode output from the drain of the upper MOSFET, which becomes the output for the Cascode configuration.

The Cascode method offers advantages for a flyback converter in a solar pump controller, including improved efficiency, higher voltage gains, reduced output impedance, and enhanced bandwidth. However, it comes with drawbacks such as increased complexity, higher component count and cost, potential increased power dissipation, and sensitivity to parameter variations. When considering the use of the Cascode method, it is crucial to carefully assess these factors and determine their impact on the specific requirements and trade-offs of the solar pump controller application.

The next step is to choose the right MOSFET for the application. Calculate the maximum current and voltage that the switch can handle. The equation can be used to calculate the maximum voltage which follows as

$$V_{DS\_max} = V_{In\_max} + \frac{D_{max} \times V_{In\_min}}{1 - D_{max}}$$

$$= 1524V + 20\% \text{ safety margin} = 1828.8V$$

Also, the maximum current passing through can be determined by,

$$I_{p\_peak} = \frac{P_{IN}}{D_{max} \times V_{In\_min}} + \frac{D_{max} \times V_{In\_min}}{2 \times f_{sw} \times L_{p\_max}}$$

$$= 628.4mA$$

So, we considered a MOSFET which has a voltage rating of 2000V and a current rating of 1A

**Step 4: MOSFET Calculation Input and Output Rectifier Diode for calculations**

For, the input diode bridge, the input current is determined by,

$$I_{In\_rms} = \frac{P_{In}}{V_{ac\_min} \times \cos \alpha}$$

Here,  $\cos \alpha$  is the power factor, which is assumed to be 0.6

$$\text{So, } I_{In\_rms} = 0.452 \text{ A}$$

$$V_{ac\_max} = (V_{ac\_max} \times 1.73) + 20\% \text{ safety margin}$$

$$= 1432.44V$$

Considering the rise in dc bus voltage due to regenerative action, two diodes of 1000 V with a 1-A rating are used for the 3-phase bridge rectifier.

Now, for the Output diode

$$V_{ds} = \frac{V_{out} + V_{In\_max}}{N_{s1}}$$

$$= 15 + \frac{1200}{20.63}$$

$$V_{ds} \Rightarrow 73.16V$$

Also, the max current at the output side is 3.7 A  
So, I have considered 100V, 5A diode for all 3 output levels.

### Step 5: Output capacitor calculation

The calculation for selecting the output ESR is based on 90% of the allowable output ripple voltage.

$$C_{out} = 20u \times \frac{P_{out}}{2 \times P_{in} \times V_{Ripple}}$$

$$\Rightarrow 180uF$$

Here, I have considered a 240uf capacitor for a filter, and for that two 120uf capacitors in parallel are connected.

### Step 6: Flyback transformer design and calculation

The next step in designing a flyback converter for a solar pump controller involves selecting the appropriate transformer. This decision involves considering factors such as the core material and shape [11]. One commonly chosen option is a ferrite core with a double E shape, which effectively concentrates the magnetic flux Fig.4. To determine the transformer's size, the AP method is employed. This method calculates the combined area of the winding window and the core cross-section where the magnetic flux is concentrated. It is crucial to carefully evaluate the specific requirements of the application, including the input voltage range, desired output voltage, maximum power, and operating frequency. Additionally, calculating the turns ratio ( $N_p$ :  $N_s$ ) based on the desired output voltage and input voltage range is a critical step in the design process.

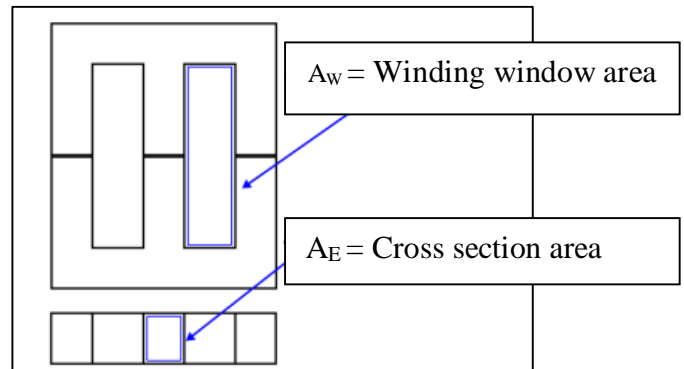


Figure 4: Transformer Internal Diagram

The transformer area can be estimated with Equation,

$$A_p = A_e \times A_w$$

From the equation, I have considered  $A_p$  as 164 mm<sup>2</sup> also the  $B_{max}$  is considered as 0.3T  
The transformer ratio is as defined in step 2.

### Step 7: Snubber design and calculation

The primary-side RCD snubber plays a vital role in flyback converter design by addressing various aspects. It protects sensitive components from voltage spikes and reduces noise, improving the overall reliability of the system. Additionally, it enhances efficiency by minimizing energy losses and helps reduce electromagnetic interference (EMI) generated during switching operations. The primary-side placement of the RCD snubber simplifies circuit layout, optimizing noise reduction and ensuring proper functionality [10].

The highest voltage that the capacitor can handle can be described as follows,

$$V_{c,max} = V_{ds,max} \times 0.1 + V_{in,min} \frac{D_{max}}{1 - D_{max}}$$

$$= 476.4V$$

The power dissipated in the snubber resistor can be expressed as follows,

$$P_{R_{snubber}} = \frac{f_{sw} \times I_p^2 \times L_{leak}}{2}$$

$$= 12.62W$$

Now, determine the snubber resistance by using the following method,

$$R_{snubber} = \frac{V_{c,max}^2}{P_{R_{snubber}}}$$

$$= 18.9k\Omega$$

$$\approx 19k\Omega$$

And, the snubber capacitor can be calculated using the following approach,

$$C_{snubber} = \frac{1}{V_{o,ripple} \times I_p \times L_{leak}}$$

$$\Rightarrow 106nF$$

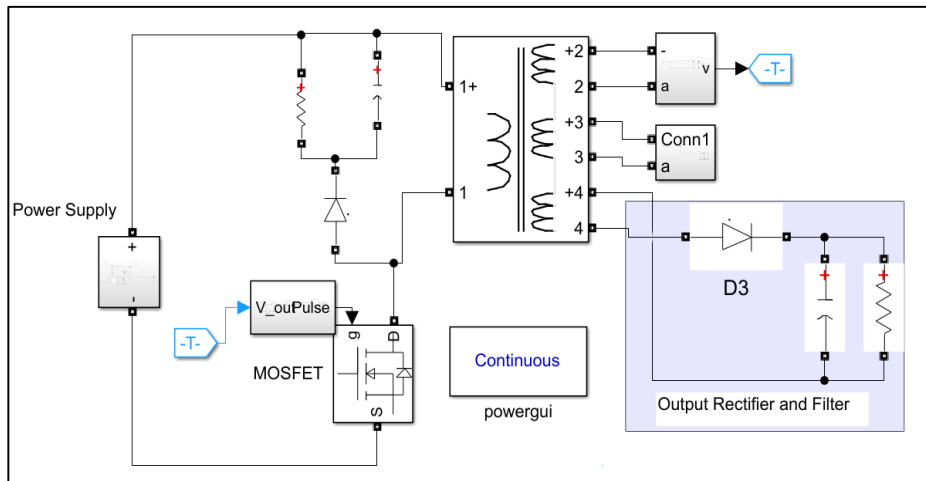
Lastly, determine the maximum voltage across the snubber diode, following Equation,

$$V_{R_{snubber}} = 1.2 \times V_{DS,max}$$

$$= 1828.8V$$

So, the diode with a 2000V 1A rating can be considered for design.



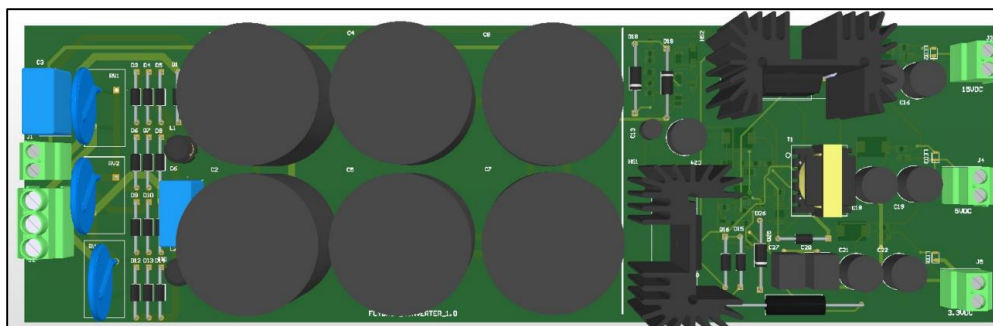
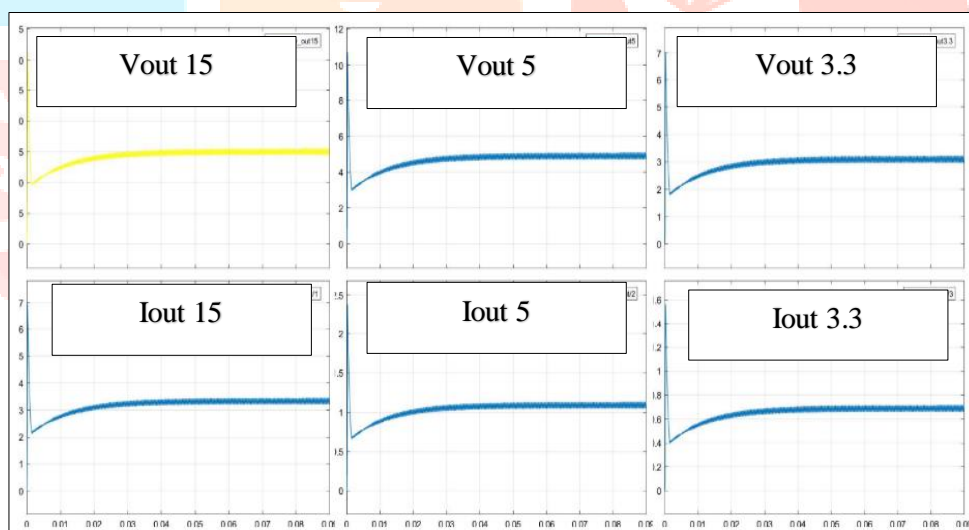


**VI. SIMULATION AND PCB LAYOUT OF THE FLYBACK CONVERTER**

With all the necessary calculations, it is mandatory to check whether the calculations are correct or incorrect before final production and to check this, we have simulated this fly-back converter into MATLAB Simulink and attached with PCB Layout Fig. 5 & 6. 3D layout of the fly-back converter is also depicted in Fig.7.

**VII. CONCLUSION**

The Cascode configuration improves the converters performance by enhancing its bandwidth, linearity and gain making it well-suited for high-frequency applications. Conducting simulations is essential for optimizing the design and assessing its performance, ensuring efficient power conversion and reliable operation. Accurate calculations such as snubber values and determining transformer parameters are crucial for achieving the desired output voltage and efficiency. By carefully considering these aspects and employing proper design techniques, the fly-back converter with Cascode method proves to be a dependable and efficient solution for solar pump controllers.



**Figure 7: 3D Layout of The Flyback Converter**

**REFERENCES**

- [1] M. H. Rashid, *Power Electronics Circuits, Devices and Applications*, 3rd edition Pearson/Prentice Hall, 2004.
- [2] Dr. P. S. Bhimra, Khanna Publishers: *Power electronics, Thyristor, IGBT, Diode*, Third edition: November, 1999.
- [3] Mohan, Undeland And Robbins, Wiley India: *Power Electronics Converters, Applications and Design*, 3rd Edition, Media Enhanced.
- [4] Pressman, A. I., Billings, K., Morey, D., & Taylor, T. *Switching Power Supply Design*. McGraw-Hill Education. *International Journal of Power Electronics and Drive Systems*, 9(1), 215-217, 2018.
- [5] Tomas Hudson, Miguel Ametller "How to Design a Flyback Converter in Seven Steps" by Monolithic Power, Rev. 1.0, 4<sup>th</sup> April 2022
- [6] Harald Zöllinger, Rainer Klin, "Line Switch Mode Power Supply (SMPS)", AN-SMPS-ICE2xXXX-1 by Infineon Technologies, Version 1.2, February 2002
- [7] Gray, P. R., Hurst, P. J., Lewis, S. H., & Meyer, R. G. (2009). *Analysis and Design of Analog Integrated Circuits* (5th ed.). Wiley.
- [8] Swati Kunkolkar<sup>1</sup>, V.N Shet," Flyback Converter Design and Simulation" in *International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering* Vol. 6, Issue 5, May 2017
- [9] T-Y Ho M-S chen, C-Hsien Lin D & A C-W chang "The design of a flyback
- [10] converter based on simulation" *IEEE on Power Electronics* 2011.
- [11] A. Marinov, E. Bekov, F. Feradov and T. Papanchev, "Genetic Algorithm for Optimized Design of Flyback Transformers," 2020 21st International Symposium on Electrical Apparatus & Technologies, Bourgas, Bulgaria, 2020, pp. 1-4, doi: 10.1109/SIELA49118.2020.9167125.
- [12] R. Singh, S. Bose V. P. Dwivedi, "Closed Loop Control of Flyback Converter with PV as a Source". *IEEE 9th Power India International Conference (PIICON)*, pp. 1-6, February 2020.
- [13] Canan Akay, Osman Yildirim, Hüseyin Çalik, Yusuf Özoğlu, Serdar Yılmaz, "Design of Flyback Converter by Obtaining the Characteristics of Polymer Based R2R Organic PV Panels" in *International Journal of Renewable Energy Research (IJRER)* C. Akay et al., Vol.12, No.4, December 2022
- [14] Suraj Desai, Prathamesh Bhatkar, Arjun Bhise, Krupal Todankar, N.N. Kasar, "Design and Implementation of Isolated Multi-Output Flyback Converter" in *International Research Journal of Engineering and Technology (IRJET)*, Volume: 05, Issue: 10, Oct 2018
- [15] TIDA-00173, "400- to 690-V AC Input 50-W Flyback Isolated Power Supply Reference Design for Motor Drives" by Texas Instruments Incorporated, September 2014–Revised May 2019
- [16] Sheng-yang Yu<sup>1</sup>, "Design considerations of high-voltage converters in a Cascode MOSFET", 3<sup>rd</sup> Nov 2015
- [17] Nasir Coruh, Satilmis Urgun, Tarik Erfidan "Design and Implementation of Fly-back Converters" 2010 5th IEEE Conference on Industrial Electronics and Applications Taichung, Taiwan, 2010, pp. 1189-1193, doi: 10.1109/ICIEA.2010.5515894.