Harmonic Mitigation Technique of Active Power Filter

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Abstract— Harmonic distortion is a form of electrical noise. It is a superposition of signals, which are of multiples of fundamental frequency. Proliferation of large power electronic systems results in increased harmonic distortion. Harmonic distortion results in reduction of power quality and system stability. This paper presents fuzzy control applicable for active power filter for three-phase systems, which are comprised of nonlinear loads. The results of the computer simulation prove that the injected harmonics are greatly reduced, system efficiency and power factor are improved.

Keywords—Fuzzy logic current controller, Harmonic current compensation, Power quality, Shunt active filter.ANN.

I. INTRODUCTION

Harmonics contamination is a serious and a harmful problem in Electric Power System. With the development of power electronics, the converters are widely used in the power supply devices and control application. These Non-linear loads draw currents that are non-sinusoidal and thus create voltage drops in distribution conductors. Typical non-linear loads based on solid-state converters are like UPS, SMPS etc Active Power filtering constitutes one of the most effective proposed solutions. Active power filter (APF) can solve problems of harmonic and reactive power simultaneously.

Shunt active filter (SAF) is one among the various types of custom power devices proposed to improve the power quality [2]. Harmonic extraction is the process in which, reference current is generated by using the distorted waveform. Many theories have been developed such as p-q theory (instantaneous reactive power theory), d-q theory, PLL with fuzzy logic controller, neural network etc..

The active filter used to compensate for the nonlinear load is a three-phase inverter. The Shunt Active Filter (SAF) is controlled by a Proportional- Integral (PI) controller and FC. They are used to shape the line current to be in phase and of the same shape as the supply voltage. The three-phase SAF configuration is simulated using MATLAB/SIMULINK. The effect of SAF on harmonic reduction is also presented.

The controller is the core of the active power filter operation and has been the subject of numerous research in recent years [9], [10]. The conventional control scheme to generate pulses, which is based on hysteresis or pwm logic control, presents several drawbacks. To improve the active power filter performance, the tendency has been to use intelligent control techniques, particularly fuzzy logic controllers. Recently, the use of fuzzy logic controllers in power electronics applications has generated considerable interest [5], [6]. The principal advantages of these controllers are robustness, ability to control nonlinear systems, absence of the need for accurate mathematical model, etc.

II. SHUNT ACTIVE POWER FILTER



Figure.1 Shunt Active Power Filter

The controller of the shunt active filter is concise and requires less computational efforts than many others found in the literature. It is formed by a dc voltage regulator, a synchronizing circuit and a compensating current reference box. Here, the PWM current control is considered as part of the power converter.

A shunt active power filter that achieves low current total harmonic distortion THD, reactive power compensation, and power factor correction is presented. The topology is based on IGBT's voltage inverter, intended to damp harmonics produced by a diode rectifier. The main paper's contribution is the use of the notch filter method, consisting solely of two serial band-pass filters, for reference currents calculation, and the application of fuzzy logic for better active filter current control accuracy.

Damping harmonics devices must be investigated when the distortion rate exceeds the thresholds fixed by the ICE 61000 and IEEE 519 standards.



Figure 2. Block diagram of Shunt Active Power Filter with proposed FLC

III. CONTROL STRATEGIES

A. Synchronous reference frame detection method

The first control strategy used in this work to compensate harmonic currents is based on the synchronous reference frame detection method. The principle of this method is described below [5]. The three phase currents *ia*, *ib* and *ic* are transformed from three phase (*abc*) reference frame to two phase's ($\alpha - \beta$) stationary reference frame currents *ia* and *i* β using:



Figure 3 Synchronous reference frame detection method

B. Synchronous current detection method

The second control strategy is inspired of the synchronous detection reference currents method [10]. It is based on the measuring of the source voltages (*Vsa*, *Vsb*, *Vsc*) given by



Figure 4. Synchronous current reference detection method

IV.HARMONIC CURRENT CONTROL METHOD

Hysteresis current control is a method of generating the required triggering pulses by comparing the error signal with that of the hysteresis band and it is used for controlling the voltage source inverter so that the output current is generated from the filter will follow the reference current waveform is shown in Figure 3.



Fuzzy Control Application

Fuzzy logic serves to represent uncertain and imprecise knowledge of the system, whereas fuzzy control allows taking a decision even if we can't estimate inputs/outputs only from uncertain predicates [15, p. 16]. Figure 8 shows the synoptic scheme of fuzzy controller, which possesses two inputs: the error (*e*), (e = iref - if) and its derivative (*de*), and one output: the command (*cde*).



Figure 5. Fuzzy controller synoptic diagram.

Figure 6. Fuzzy control construction.

Figure 6. illustrates stages of fuzzy control in the considered base of rules and definitions: fuzzyfication, inference mechanism, and defuzzyfication.

Fuzzy logic current controller

The main component of an active filter is the current controller. Generally the classical hysteresis controller is used to generate pulses to the PWM inverter; it is very stable and

generates a minimum noise. Recently, fuzzy logic controllers (FLCs) have been interest a good alternative in more application. The advantage of fuzzy systems are that they do not need an accurate mathematical model, they can work with imprecise inputs, can handle non-linearity, and they are more robust than conventional nonlinear controllers [12],[13].

Fuzzy logic control is the evaluation of a set of simple linguistic rules to determine the control action. To develop the rules of the fuzzy logic, we need good understand of the process to be controlled, but it does not require a complicated mathematical model. The desired switching signals for the filter inverter circuit are determined according to the error in the filter current. In this case, the fuzzy logic current controller has two inputs, named error e and change of error de and one output named s. Here the error e and change of error de are the

input variable for the system [18]. To convert it into linguistic variable, we use three fuzzy sets:

N (Negative), ZE (Zero) and P (Positive). Figure 8. shows the membership functions used in fuzzification.

The fuzzy controller for every phase is characterized for the following:

- Three fuzzy sets for each input,
- Five fuzzy sets for each output,
- Triangular and trapezoidal membership functions,
- Implication using the "min" operator,
- Mamdani fuzzy inference mechanism based on fuzzy implication.
- Defuzzification using the "centroid" method.

The linguistic rules for the fuzzy current controller are as follows:

1. If error is Negative and error rate is Negative Then output is Big Negative,

2. If error is Zero and error rate is Negative Then output is Positive,

3. If error is Positive and error rate is Negative Then output is Big Positive,

4. If error is Negative and error rate is Negative Then output in Big Negative,

5. If error is Zero and error rate is Zero Then output is Zero,

6. If error is Positive and error rate is Zero Then output is Big Positive,

7. If error is Negative and error rate is Positive Big Then output is Big Negative,

8. If error is Zero and error rate is Positive Then output is Negative,

9. If error is Positive and error rate is Positive Then output is Big Positive.



Figure 7. Shunt active filter based on Fuzzy current controller



Figure 8. Membership function for the inputs and output variables

The generation process of switching signals is given by Figure 9.



Simulation software

For simulation studies, we used SIMULINK toolbox under MATLAB software in order to model and test the system under investigation.

V. SIMULATION RESULTS AND DISCUSSION

The purpose of the simulation is to show the effectiveness of the shunt active filter using intelligent controllers (fuzzy logic and ANN current controllers) to reducing the harmonic currents produced on the load side under ideal voltages conditions. The model parameters used for simulation are:

Voltage source Vs=220V, Frequency Fs=50Hz, Resistor Rs=0.1m Ω , Inductance Ls=0.0002mH, Resistor Rch= 48.6Ω , Inductance Lch=40mH, DC Voltage Udc-ref=700V, Capacitance Cdc= 3000μ F, Resistor Rc=0.27m Ω , Inductance Lc=0.8mH.

Figure 8 shows the voltage source and line current before compensation





Figure 10. Voltage source and line current before compensation



Figure 11. Source current spectrum before compensation

Figure 12. Source current using Fuzzy Logic controller



Figure. 15 DC capacitor voltage

VI.CONCLUSION

In this paper, a three-phase three-wire shunt active filter based on intelligent controllers using two control strategies is proposed to compensate current harmonics. The first controller based on FLC use the synchronous reference frame detection strategy. The second controller based on ANNs use the synchronous current detection strategy. The dc-link voltage is regulated using a proportional integral voltage controller with fast dynamic response in case of load

current variation. The Simulation results obtained show the robustness and the effectiveness of the two proposed intelligent controllers. After compensation the source current is balanced, sinusoidal and in phase with line voltage source. The harmonic spectrum shows that the THD is very acceptable and respect IEEE standard Norms.

VII.REFERENCES

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